|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **A** | **B** | **C** | **f() Boolean equation** | **AND/OR Circuit** | **NAND Circuit** |
| **0** | **0** | **0** | ABC | 1 | 1 |
| **0** | **0** | **1** | ABC’ | 0 | 0 |
| **0** | **1** | **0** | AB’C | 1 | 1 |
| **0** | **1** | **1** | AB’C’ | 1 | 1 |
| **1** | **0** | **0** | A’BC | 1 | 1 |
| **1** | **0** | **1** | A’BC’ | 0 | 1 |
| **1** | **1** | **0** | A’B’C | 1 | 1 |
| **1** | **1** | **1** | A’B’C’ | 0 | 0 |